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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/915,109	07/25/2001	Lynn P. West	TNSY:020US (F&J 10105492)	5532
25094	7590	04/06/2004	EXAMINER	
GRAY, CARY, WARE & FREIDENRICH LLP 1221 SOUTH MOPAC EXPRESSWAY SUITE 400 AUSTIN, TX 78746-6875			ELAMIN, ABDELMONIEM I	
			ART UNIT	PAPER NUMBER
			2116	7
DATE MAILED: 04/06/2004				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/915,109

Applicant(s)

WEST, LYNN P.

Examiner

A I Elamin

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 25 July 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-46 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-15, 21-28 and 43-46 is/are rejected.
- 7) ☒ Claim(s) 16-20 and 29-42 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>2,5,6</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims *1-15, 21-28 and 43-46*, are rejected under 35 U.S.C. 102(e) as being anticipated by Anderson et al, US. Pat. No. 6,594,711.

3. Claims *1, 3-6, 22-26 and 43-46*, Anderson teaches a method [*title, abstract*], comprising:
 - interconnecting a compute node [*CPU 411, 412, 413, 414 of Fig. 4*] with a shared memory node [*abstract*];
 - translating a processor instruction into an interconnect command [*col. 8, lines 1-52, col. 11, lines 18-60*];
 - transforming the interconnect command into DMA interconnect command [*col. 8, lines 1-52, col. 11, lines 18-60*];
 - transmitting the DMA interconnect command via a link medium [*Fig. 4*]; and
 - performing an operation defined by the DMA interconnect command [*abstract, col. 3, lines 1-35*].

4. Claim 2, Anderson teaches the shared memory node is not immediately adjacent to the compute node [*Fig. 4*].

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5. Claim 7, Anderson teaches the link medium includes a serial link medium [*col. 9, lines 36-45*].

6. Claims 8-15, Anderson teaches the interconnect command defines a format ... [*col. 8, line 53 thru col. 9, line 34*].

7. Claim 21, Anderson teaches interconnecting includes utilizing at least one ... [*col. 9, lines 36-45*].

8. Claim 27, Anderson teaches the compute node interconnect interface unit includes; a processor bus; a memory bus ... [*Figs. 5-7*].

9. Claim 28, Anderson teaches the shared memory interconnect interface unit includes; a processor bus; ... [*Fig. 4*].

Allowable Subject Matter

10. Claims 16-20 and 29-42 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to A I Elamin whose telephone number is (703)305-3804. The examiner can normally be reached on MON-FRI 9:30 AM - 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey Gaffin can be reached on (703) 308-3301. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

April 4, 2004



A. I. ELAMIN
PRIMARY EXAMINER

A I Elamin
Primary Examiner
Art Unit 2182